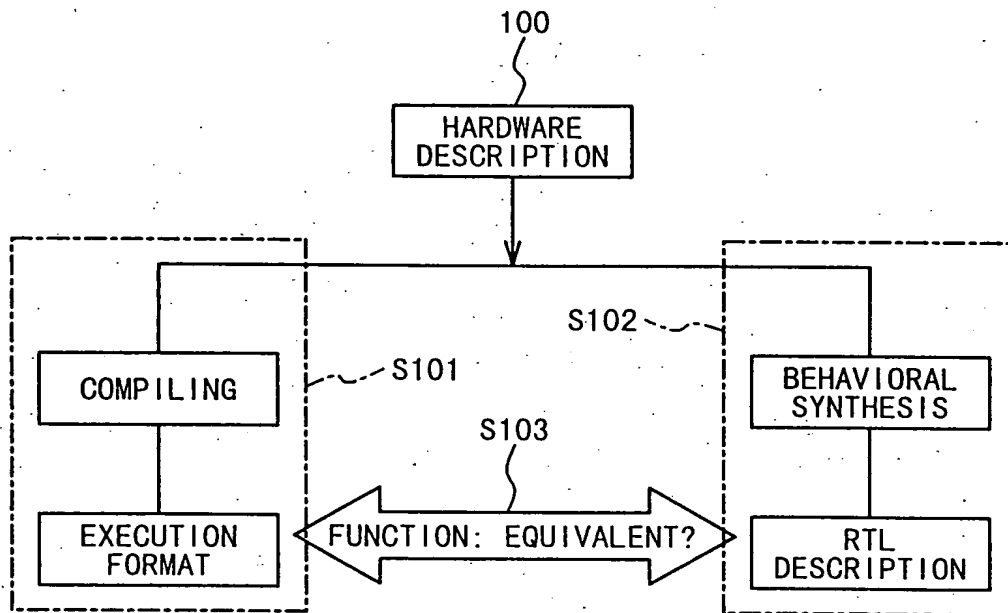




Fig. 1 PRIOR ART





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Fig. 2

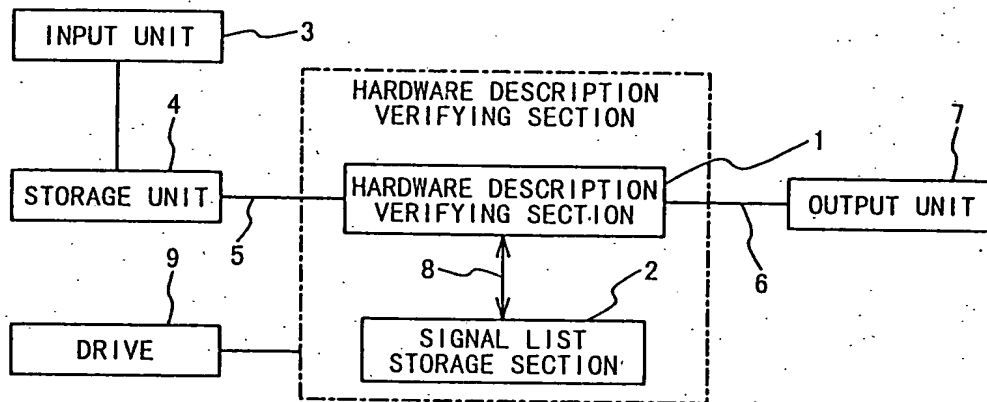


Fig. 3

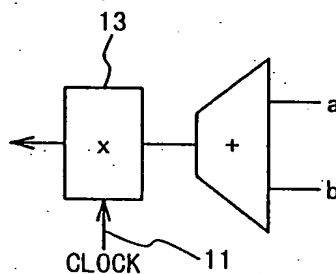
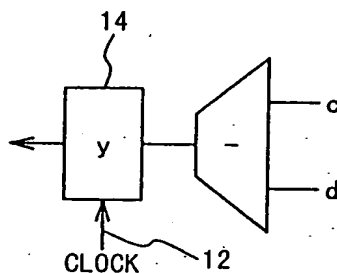


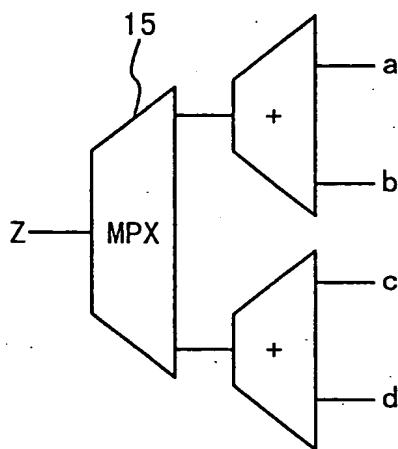
Fig. 4





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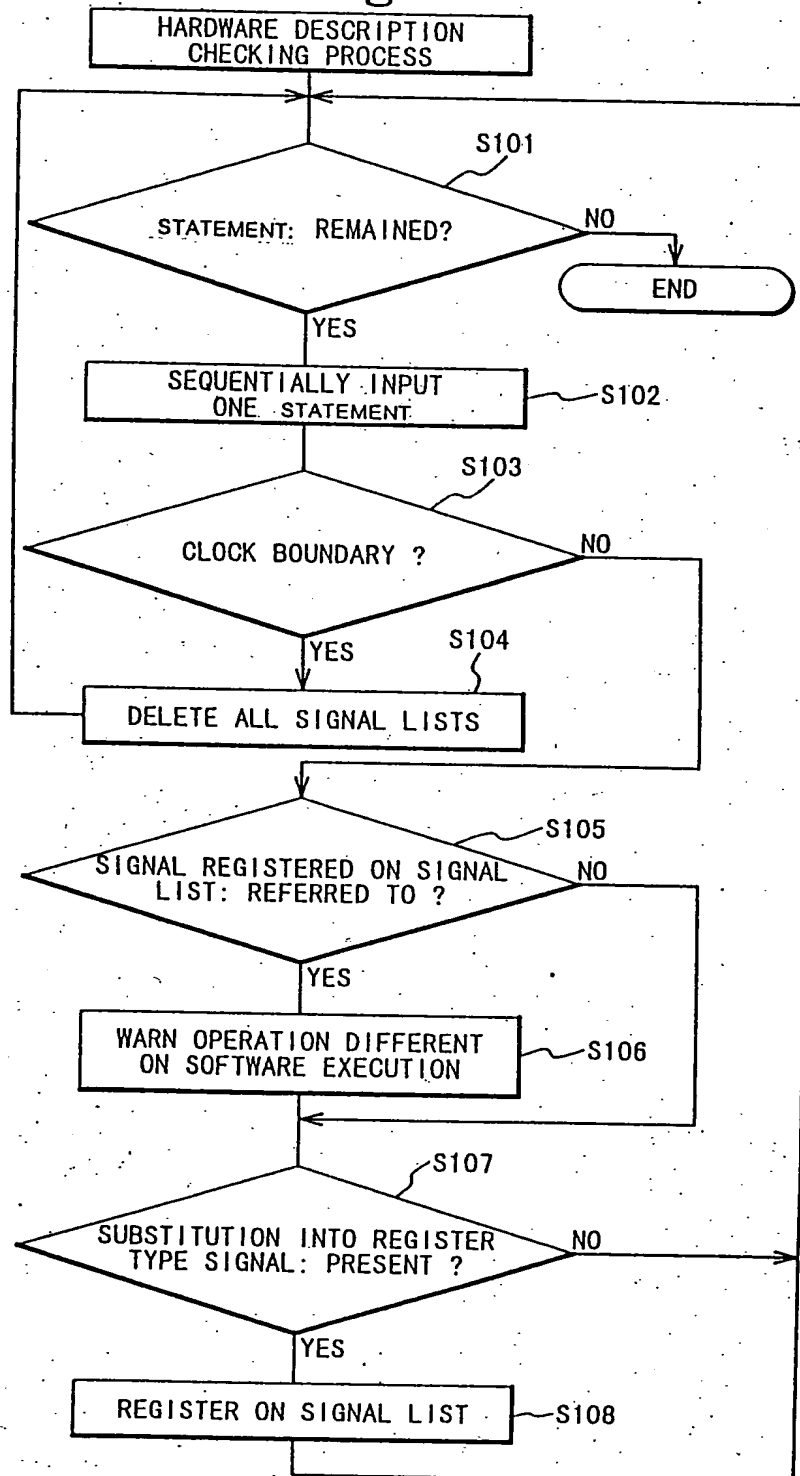
Fig. 5





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Fig. 6





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Fig. 7

```
/* REG. C */
#IFDEF C
# DEFINE REG UNSIGNED INT
#ENDIF

MAIN()

/* C */
/* HDL */
/* LIST */

INT T;
REG x, y;

x = 0;
CLOCK();
x = 1;
t = 3;
y = x + t;
CLOCK();

/* x=0 */
/* x=1 */
/* t=3 */
/* y=4 */

/* {x} */
/* {} */
/* {x} */
/* {x} */
/* {x,y} */
/* {} */

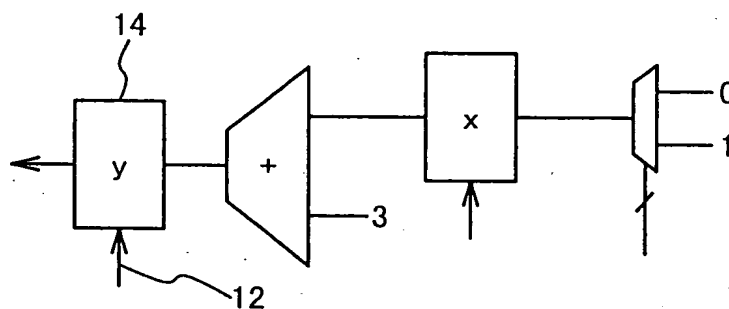
/* S107 */
/* S103 */
/* S107 */

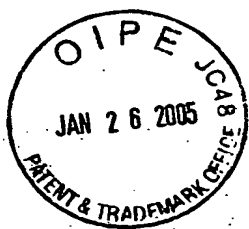
/* S105, S107 */
/* S103 */
```



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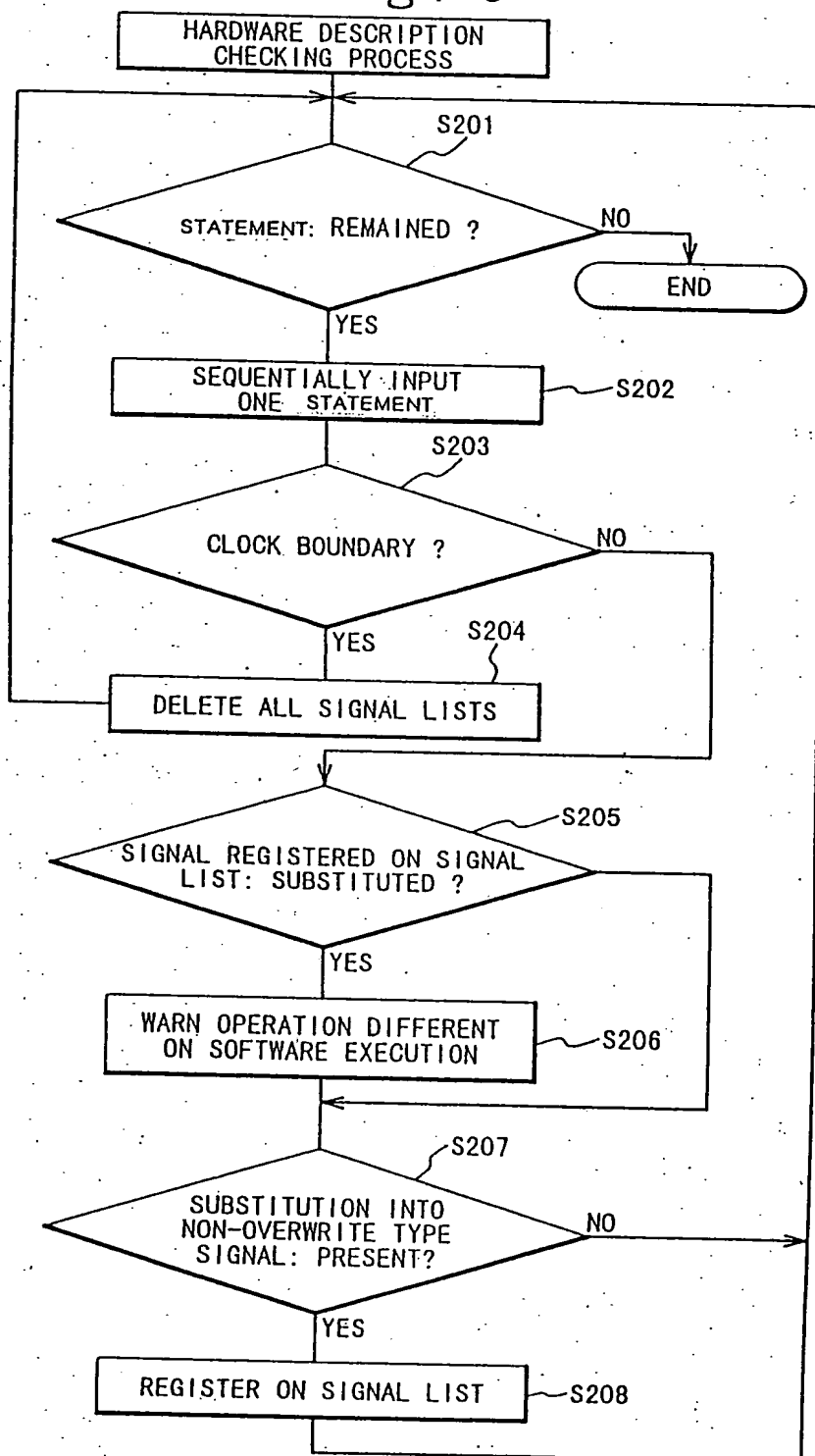
Fig. 8

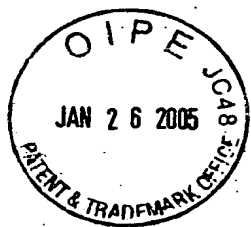




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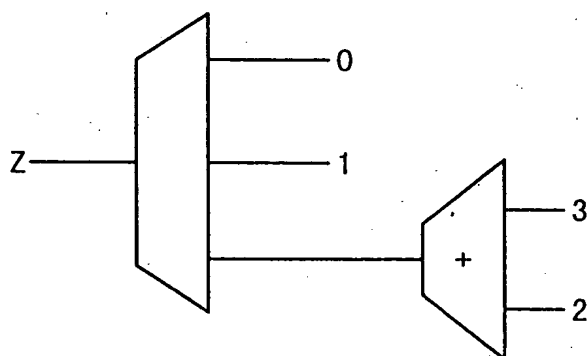
Fig. 9





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Fig. 11



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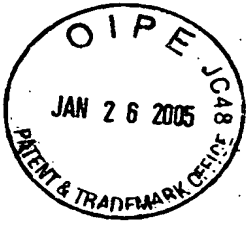


Fig. 12

```
/* TER.C */  
  
#IFDEF C  
# DEFINE TER UNSINED INT  
#ENDIF  
  
MAIN()  
{  
    TER z, t;  
  
    t = 3;  
    CLOCK();  
    z = t + 2;  
    CLOCK();  
  
    /* C */  
  
    /* HDL */  
  
    /* LIST */  
  
    /* {t} */  
    /* {} */  
    /* {z} */  
    /* {} */
```

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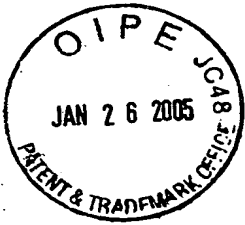
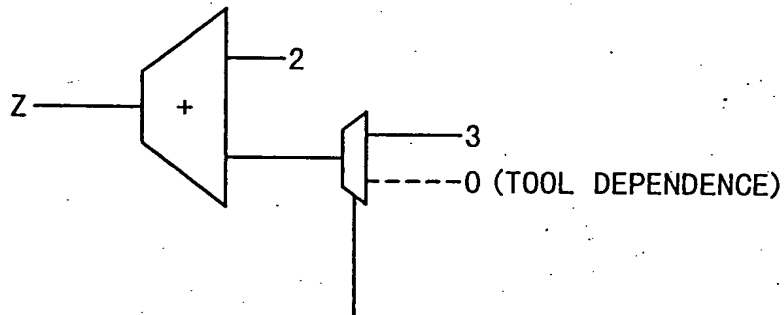
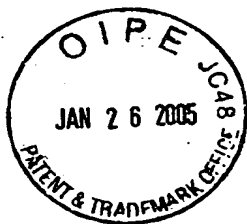


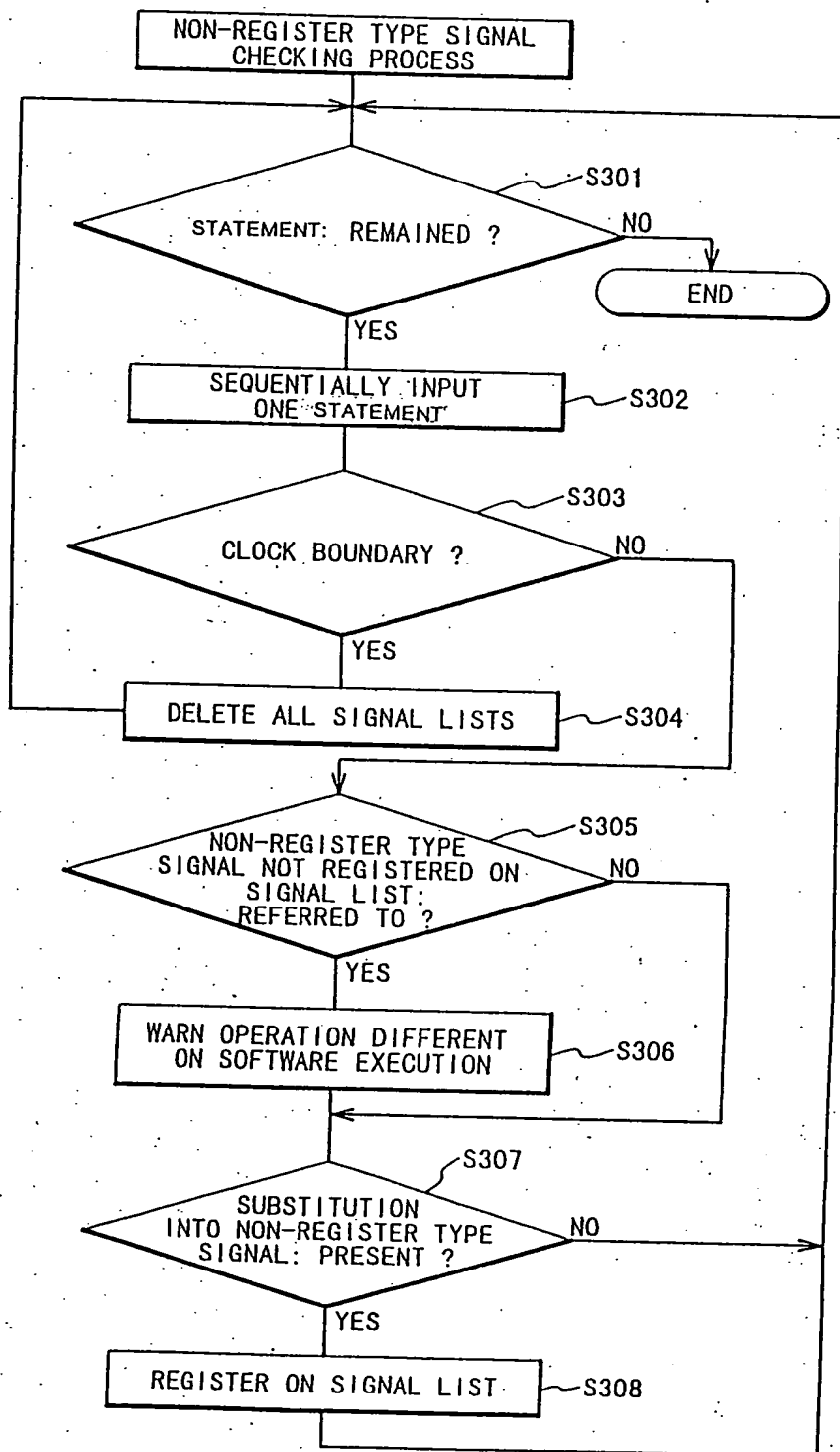
Fig. 13

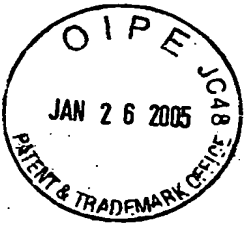




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Fig. 14





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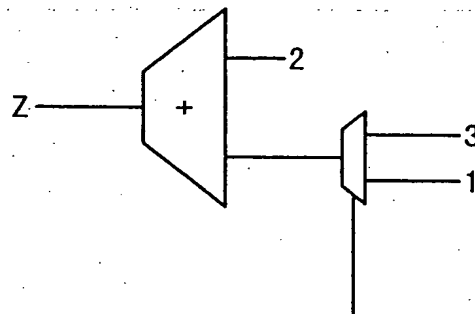
Fig. 15

```
/* WIRE.C */  
  
#IFDEF C  
# DEFINE TER UNSIGNED INT  
#ENDIF  
  
MAIN()  
{  
    TER z, t;  
  
    t = 3;  
    CLOCK();  
    z = t + 2;  
    t = 1;  
    CLOCK();  
  
    /* C */  
  
    /* HDL */  
  
    /* LIST */  
  
    /* {t} */  
    /* {} */  
    /* {z} */  
    /* {z, t} */  
    /* {} */
```

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Fig. 16



REPLACEMENT SHEET

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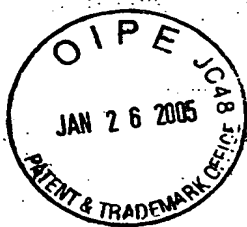
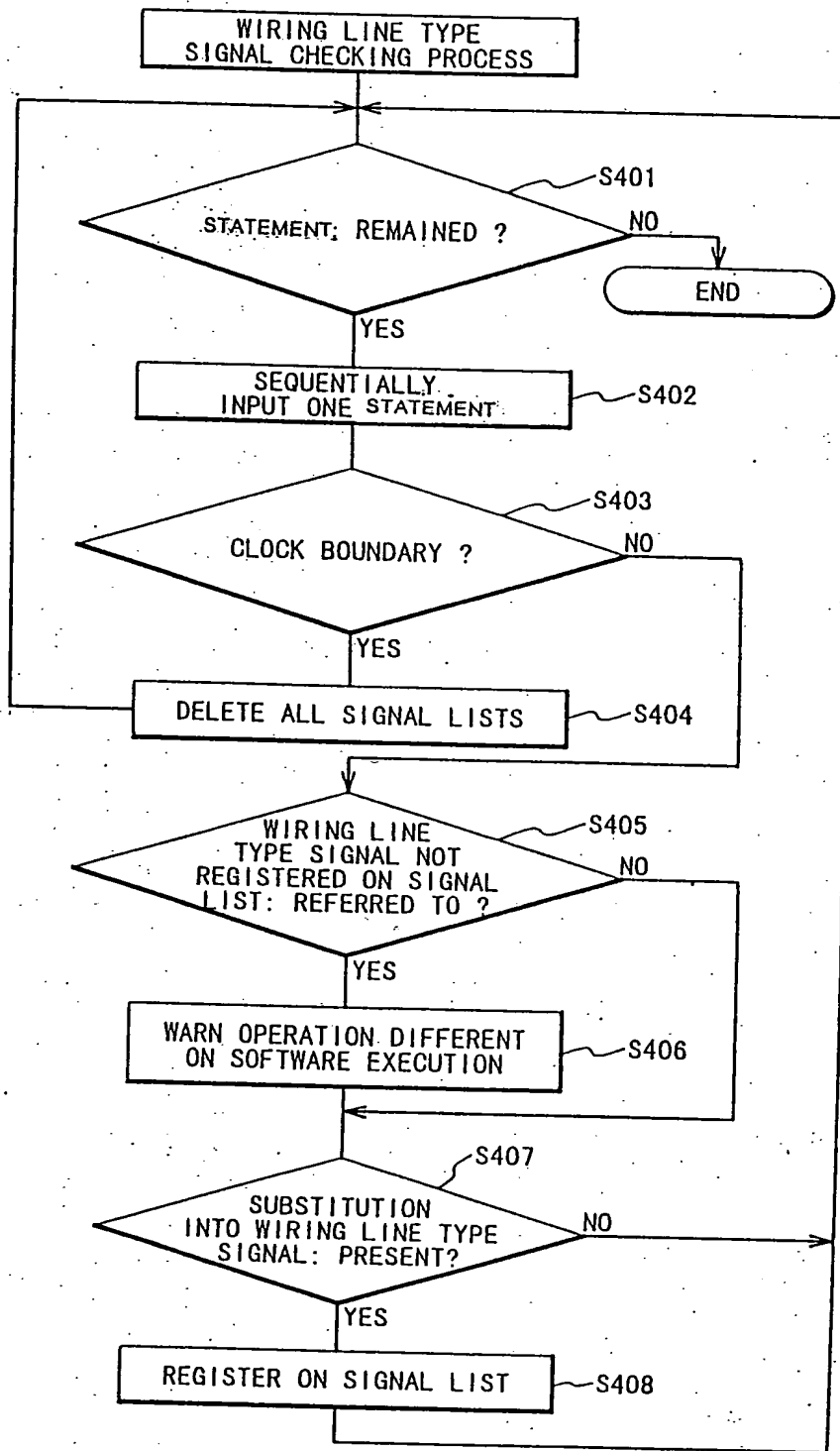
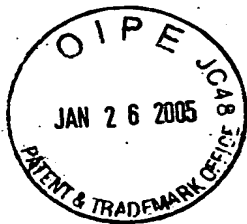


Fig. 17





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Fig. 18

```
/* AND.C */  
  
MAIN() {  
    INT a,i;  
  
    i = 0;  
    a = 0;  
    CLCOK();  
    if(i > 0 && a++) {  
        i = 0;  
    }  
    CLCOK();  
  
    /* C */  
  
    /* HDL */  
  
    /* i=0 */  
    /* a=0 */  
  
    /* a=1 */
```




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Fig. 19

